

Form PTO-1449

DEC 24 2003

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Doctor Number (Optional)

TSMC-02-1091

Application Number

10/669, 515

Applicant

Wen-Tai Wang et al.

Filing Date

09/24/03

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28/6

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Com	Article "Interconnect and Circuit Modeling Techniques for Full-Chip Power Supply Noise Analysis," by Chen et al., in IEEE Trans. on Components, Packaging, and Manuf. Tech.-Part C, Vol. 21, No. 3, Aug. 1998, pp. 209-215.
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EXAMINER

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DATE CONSIDERED

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

